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Lehrerversion Digitaltechnik - Werkstätte

Logikfamilien: 1.1) TTL-Schaltkreisfamilie Die Bezeichnung TTL Bedeutet Transistor-Transistor-Logic. Die Verknüpfungen Werden Bei Dieser Schaltkreisfamilie Ausschließlich Durch Bipolare Transistorstufen Erzeugt. Zur Verschiebung Von Pegel Und Zur Spannungsableitung Werden Dioden Verwendet. Widerstände Dienen Als Spannungsteiler Und Strombegrenzer. Die TTL-Schaltkreisfamilie Gibt Es In ... Mar 8th, 2020

Mitschrift Mikrocomputer

1.5 Logikfamilien Unterscheidungsmerkmale Halbleitertechnologie (CMOS, Bipclon, BICMOS, ...) Betriebsspannung Low-High Pegel Leistungsaufnahme Geschwindigkeit Treiberleistung Csingle-ended Differential Terminierung 1.6 Logikbausteine 1.6.1 Bezeichnungssystematik 1.6.2 Beispiele Der 74er Serie 18.12.2004 Seite 6 Von 44 Y X P Start T LKC. Mitschrift Mikrocomputer 2 CPU 2.1 Grundlagen ... Mar 7th, 2020

Elektronik-Praktikum - Uni-giessen.de

Abbildung 1.1: Inverter Mit Pegel 1.2 Logikfamilien Bei Der Technischen Realisierung Der Logikschaltungen Müssen Unterschiedliche Gesichtspunkte Betrachtet Werden. Wichtige Kriterien Sind Schaltgeschwindigkeit, Stromverbrauch, Aufwand, Preis, Platzbedarf Und Vieles Mehr. Die Unterschiedlichen Verfahren Bezeichnet Man Mit Logikfamilien. Eine Einfache Realisierung Eines Inverters Erhält ... Jan 20th, 2020

Einsatzmöglichkeiten Und Grenzen Der SN54/74HCT CMOS-Logikfami

Wie Man Sieht, Ist Der Störabstand Beim Low-Pegel S L Bei Allen Drei Logikfamilien Der Kritischere Wert. Er Reicht Von 18,7% Bei HC- Bis 12,9 % Bei TTL-Schaltungen. Von Daher Zeigen HC-Schaltungen Ein Deutlich Besseres Verhalten Als Bipolare Logikschaltungen. Von Ausschlaggebender Bedeutung Ist Aber In Der Praxis, In Wie Weit Die Einzelnen Schaltungen In Der Lage Sind, Störungen, Die In Eine ... May 5th, 2020

Bitübertragungsschicht

Kompatibel Zu Den Gängigen Logikfamilien 1.1.2.4 Hardware-Grundlagen Wie

Bereits Beschrieben, Besteht Der I2C-Bus Aus Zwei Aktiven Leitungen SDA Und SCL So-wie Der Masseleitung Als Bezugspotential. Die Leitungen SDA Und SCL Sind über Pullup-Wi- Derstände Mit Der Betriebsspannung Verbunden. Im Ruhezustand Befinden Sich Also Beide Leitungen Auf High-Pegel (positive Logik). Die Un-ten Stehende ... May 17th, 2020

Kapitel 3 Logikschaltkreise Und Logikbaureihen

Technologien, Baureihen Und Logikfamilien Behandeln, Die In PC-Hardware Und In Anderen Modernen Hochleistungssystemen Zum Einsatz Kommen. Logikbausteine Es Lag Von Anfang An Nahe, Komplizierte Digitale Einrichtungen Aus Vorgefertigten Einheitlichen Bausteinen Zusammensetzen. Bereits Die Pioniere Der Rechentechnik (Babbage, Zuse) Haben Dies Getan - Zunächst Auf Grundlage Der Mechanik. Aber ... Aug 11th, 2020

3 Digitale Bauelemente Realisierung

3.2 Logikfamilien Schaltungstechnische Umsetzung Einer Logik Jede Familie Hat Spezi Sche Eigenschaften: Pegelh Ohe Und Versorgungsspannung St Orabstand Gatterlaufzeit / Maximale Frequenz Leistungsaufnahme Veraltete Familien: 1. Widerstand-Transistor-Logik (RTL) 2. Dioden-Transistor-Logik (DTL) 3. Langsame St Orsichere Logik (LSL), Pegel 15V Jun 4th, 2020

Allgemein TTL CMOS

November 2006 "Logikfamilien" Von Martin Krellmann 2 Allgemein Definition Und Funktionsweise (P-und N-Logik) TTL Funktionsbeschreibung Low-Power-Schottky-TTL-Gatter OpenCollector-Ausgänge Tristate-Ausgänge CMOS Beispiel Im Projekt: Taster Mit RS-FlipFlop in CMOS Logik DTL Logik Am Beispiel Eines NOR-Gatters Verschiedene Familien In Einer Schaltung. 24. November 2006 "Logikfamilien" Von Martin ... Nov 1th, 2020

Logikfamilien Der Digitaltechnik - DK4EK

L-Pegel Geschaltet Wird. Nur Dann, Wenn Beide Eing Ange Auf L-Potential Gelegt Wird, Sind V2 Und V3 Gleichzeitig Gesperrt. In Diesem Fall Wird V5 Über Den 1 ;6k-Widerstand Angesteuert Und Damit Leitend, W Ahrend Die Basis Von V6 Über Den 1k-Widerstand Auf 0 Volt Gelegt Wird, Wodurch V6 Sperrt. Der Ausgang Zeigt H-Potential. Wir Erhalten Diese ... Jan 1th, 2020

CMOS - Standardlogik

CMOS-5V-PEGEL: $V_{IH} = 3.5V$ (Störabstand Etwa 1V) Die Ersten Drei Logikfamilien Mit Den Typenbezeichnungen 4000 Und Aufwärts Besitzen Von Den TTL Abweichende Funktionen, Funktionsbezeichnungen Und Pinbelegungen. Die Ersten Drei Familien Erlauben Versorgungsspannungen Bis Zu 18V (Logikfamilien Werden In Silicon Gate Prozessen Gefertigt (niedrigere Versorgungsspannung, Aber Wesentlich Schneller ... Sep 22th, 2020

Logikfamilien (logic Families) - Projektlabor Forum

Logikfamilien (logic Families) Referent: Sebastian Becker 28.06.2006. 28.06.2006

Sebastian Becker 2 Gliederung 1. Digitale Schalter 2. TTL 3. CMOS 4. Vergleich Der Logikfamilien 5. Pegelanpassung 6. Interfacen . 28.06.2006 Sebastian Becker 3 Logik-Schaltungen. 28.06.2006 Sebastian Becker 4 Der Bipolar-Transistor Als Schalter. 28.06.2006 Sebastian Becker 5 Diode -transistor Logic (DTL) AND ... Jan 8th, 2020

Logikfamilien

Logikfamilien Logikfamilien 1) TTL-Schaltung 1.1) Funktion Die Bezeichnung TTL Bedeutet Transistor-Transistor-Logik Und Beschreibt, Dass Sowohl Die Signal-Ein-, Als Auch Die Auskopplung über Transistoren Erfolgt. Wesentliches Merkmal Der TTL-Schaltung Ist Am Eingang Der Multi-Emitter-Transistor. Er Ist In Allen Schaltkreisen Der Standard-TTL-Reihen 74xx Zu Finden, Egal, Wie Komplex Deren ... Jun 16th, 2020

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L-Pegel Geschaltet Wird. Nur Dann, Wenn Beide Eingänge Auf L-Potential Gelegt Wird, Sind V2 Und V3 Gleichzeitig Gesperrt. In Diesem Fall Wird V5 Über Den 1 ;6k-Widerstand Angesteuert Und Damit Leitend, Wodurch Die Basis Von V6 Über Den 1k-Widerstand Auf 0 Volt Gelegt Wird, Wodurch V6 Sperrt. Der Ausgang Zeigt H-Potential. Wir Erhalten Diese ... Feb 16th, 2020

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ASOSIASI KONTRAKTOR LISTRIK DAN MEKANIKAL INDONESIA (AKLI) TENTANG SERTIFIKAT TENAGA AHLI Pada Hari Ini Hari Rabu 19 Maret 2003 Kami Yang Bertanda Tangan Dibawah Ini : 1. Ir. Mangambari Tompo, MBA : Ketua Umum Pengurus Asosiasi Profesionalis Elektrikal Indonesia (APEI). Yang Berkedudukan Di Jl. Kwini I/B4 Jakarta Pusat. Yang Dalam Hal Ini Bertindak Untuk Dan Atas Nama Asosiasi Profesionalis ... Oct 24th, 2020

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3 Hours Maximum Marks : 100 [Attempt All Questions. Each Question Carries 1 Mark. There Is No Negative Mark For Incorrect Answers.] Q.1. Which Of These Is Not An Objective Of Cost Accounting? Jul 4th, 2020

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